

LFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Srinivasan Subramanian et al.  
Assignee: Sun Microsystems, Inc.  
Title: METHOD AND APPARATUS FOR MEMORY REDUNDANCY AND  
RECOVERY FROM UNCORRECTABLE ERRORS  
Serial No.: 10/624,829 Filed: July 21, 2003  
Examiner: Albert Decady Group Art 2133  
Unit:  
Docket No.: P-9643

Monterey, CA  
July 21, 2005

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER §1.97(b)**

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant(s) wish to call the following documents (a copy of each non-US Patent document is enclosed) to the attention of the Examiner:

**U.S. PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	NAME
1)	2002/0042893	04/11/2002	Larson et al.

**FOREIGN DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY
1)	GB 1 512 220	10/29/1975	GB

**OTHER DOCUMENTS**

1)	Satya et al., "Electronic Reorganization of Memory for Handling Uncorrectable Memory Errors", <u>IBM Technical Disclosure Bulletin</u> , Vol 22, No 12, pages 5378-5381, May 1980.
----	------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------



A PTO form 1449 listing these documents is enclosed. Also enclosed is a copy of the GB Search Report for Application No. GB 0319572.4 (4 pages), in which the above references were cited.

Citation of the above documents shall not be construed as:


1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

The Commissioner is hereby authorized to charge any fees required for consideration of this Information Disclosure Statement, and to credit any overpayment of fees to Deposit Account No. 50-0553.

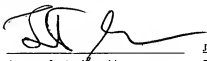
**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 21, 2005.

Respectfully submitted,



Forrest Gunnison  
Attorney for Applicant(s)  
Reg. No. 32,899  
(831) 655-0880



Attorney for Applicant(s)

July 21, 2005

Date of Signature

Form PTO-1449

Atty Docket No.

Serial No.

P-9643

10/624,829

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

Applicant(s)

Srinivasan Subramanian et al.

Filing Date

July 21, 2003

Group

2133

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	2002/0042893	04/11/2002	Larson et al.	714	7	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

## FOREIGN PATENT DOCUMENTS

							Translation	
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	AL	GB 1 512 220	10/29/1975	GB				
	AM							
	AN							
	AO							
	AP							

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AR	Satya et al., "Electronic Reorganization of Memory for Handling Uncorrectable Memory Errors", IBM Technical Disclosure Bulletin, Vol 22, No 12, pages 5378-5381, May 1980.
AS	
AT	

Examiner

Date Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

